WEST	
 Generate Collection	Drint

L9: Entry 19 of 60

File: USPT

Nov 19, 2002

DOCUMENT-IDENTIFIER: US 6484239 B1

TITLE: Prefetch queue

## Detailed Description Text (22):

When a received address misses the prefetch queue, the prefetch queue stores data from the newly received request in the queue. The prefetch queue may allocate a register and write the page address and cache address portions of the received address into respective page and home address portions of the allocated register speculatively (Step 1030). The prefetch queue may identify a prefetch direction as forward (Step 1040). The prefetch queue also may initialize the flag portion of the allocated register speculatively (Step 1050). Further, the prefetch queue may indicate that no prefetch pattern has been identified for the newly stored address (Step 1060).